


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alt](#)

Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "((((element\*, data, portion\*) &lt;and&gt; vector\* &lt;and&gt; (transfer\*, mov\*, sent , send\*))&amp;..."

[e-mail](#)

Your search matched 10 of 865 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

Search Options

[View Session History](#)[New Search](#)

Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STO IEEE Standard

Modify Search

((((element\*, data, portion\*) &lt;and&gt; vector\* &lt;and&gt; (transfer\*, mov\*, sent , send\*))&amp;...

[Search](#)☐ Check to search only within this results set

Display Format:



Citation



Citation &amp; Abstract

[view selected items](#)[Select All](#) [Deselect All](#)**1. A single-chip 1.6 billion 16-b MAC/s multiprocessor DSP**

Ackland, B.; Anesko, A.; Brinthaup, D.; Daubert, S.J.; Kalavade, A.; Knobloch, J.; Micca, E.; Motur O'Neill, J.H.; Othmer, J.; Sackinger, E.; Singh, K.J.; Sweet, J.; Terman, C.J.; Williams, J.;  
[Custom Integrated Circuits, 1999. Proceedings of the IEEE 1999](#)  
 16-19 May 1999 Page(s):537 - 540  
 Digital Object Identifier 10.1109/CICC.1999.777338

[AbstractPlus](#) | Full Text: [PDF](#)(420 KB) IEEE CNF[Rights and Permissions](#)**2. 3D-Flow with fewer than 100 K gates versus other processors for DAQ and Level-1 trigger**

Crosetto, D.;  
[Nuclear Science, IEEE Transactions on](#)  
 Volume 42, Issue 4, Part 1-2, Aug 1995 Page(s):854 - 859  
 Digital Object Identifier 10.1109/23.467780

[AbstractPlus](#) | Full Text: [PDF](#)(632 KB) IEEE JNL[Rights and Permissions](#)**3. 3D-Flow with less than 100 K gates versus processors with millions of transistors for DAQ a**

Crosetto, D.;  
[Nuclear Science Symposium and Medical Imaging Conference, 1994. 1994 IEEE Conference Rec](#)  
 Volume 2, 30 Oct.-5 Nov. 1994 Page(s):994 - 998 vol.2  
 Digital Object Identifier 10.1109/NSSMIC.1994.474455

[AbstractPlus](#) | Full Text: [PDF](#)(472 KB) IEEE CNF[Rights and Permissions](#)**4. Algorithmic mapping of neural network models onto parallel SIMD machines**

Lin, W.-M.; Prasanna, V.K.; Przytula, K.W.;  
[Computers, IEEE Transactions on](#)  
 Volume 40, Issue 12, Dec. 1991 Page(s):1390 - 1401  
 Digital Object Identifier 10.1109/12.106224

[AbstractPlus](#) | Full Text: [PDF](#)(980 KB) IEEE JNL[Rights and Permissions](#)**5. A fixed-point multimedia DSP chip for portable multimedia services**

Ong, S.; Sunwoo, M.H.; Hong, M.;  
[Signal Processing Systems, 1998. SIPS 98. 1998 IEEE Workshop on](#)  
 8-10 Oct. 1998 Page(s):94 - 102  
 Digital Object Identifier 10.1109/SIPS.1998.715772

[AbstractPlus](#) | Full Text: [PDF\(547 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



**6. A MDSP (multimedia DSP) chip for portable multimedia applications**

SooHwan Ong; HyunJune Yoo; Sunwoo, M.H.;

[Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on](#)

Volume 4, 30 May-2 June 1999 Page(s):283 - 286 vol.4

Digital Object Identifier 10.1109/ISCAS.1999.779997

[AbstractPlus](#) | Full Text: [PDF\(352 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



**7. Estimating multimedia instruction performance based on workload characterization and me:**

Gheewala, A.; Peir, J.-K.; Yen-Kuang Chen; Lai, K.;

[Workload Characterization, 2002. WWC-5, 2002 IEEE International Workshop on](#)

25 Nov. 2002 Page(s):98 - 106

[AbstractPlus](#) | Full Text: [PDF\(522 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



**8. A fully parallel vector-quantization processor for real-time motion-picture compression**

Nakada, A.; Shibata, T.; Konda, M.; Morimoto, T.; Ohmi, T.;

[Solid-State Circuits, IEEE Journal of](#)

Volume 34, Issue 6, June 1999 Page(s):822 - 830

Digital Object Identifier 10.1109/4.766816

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1712 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



**9. Vector fields for task-level distributed manipulation: experiments with organic micro actuators**

Bohringer, K.-F.; Suh, J.W.; Donald, B.R.; Kovacs, G.T.A.;

[Robotics and Automation, 1997. Proceedings, 1997 IEEE International Conference on](#)

Volume 2, 20-25 April 1997 Page(s):1779 - 1786 vol.2

Digital Object Identifier 10.1109/ROBOT.1997.614410

[AbstractPlus](#) | Full Text: [PDF\(1088 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)



**10. A realtime software MPEG transcoder using a novel motion vector reuse and a SIMD optimization**

Senda, Y.; Harasaki, H.;

[Acoustics, Speech, and Signal Processing, 1999. ICASSP '99. Proceedings, 1999 IEEE International](#)

Volume 4, 15-19 March 1999 Page(s):2359 - 2362 vol.4

Digital Object Identifier 10.1109/ICASSP.1999.758412

[AbstractPlus](#) | Full Text: [PDF\(312 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)

[Help](#) [Contact Us](#) [Privacy](#)

© Copyright 2006 IEEE

indexed by  
**inspec**